

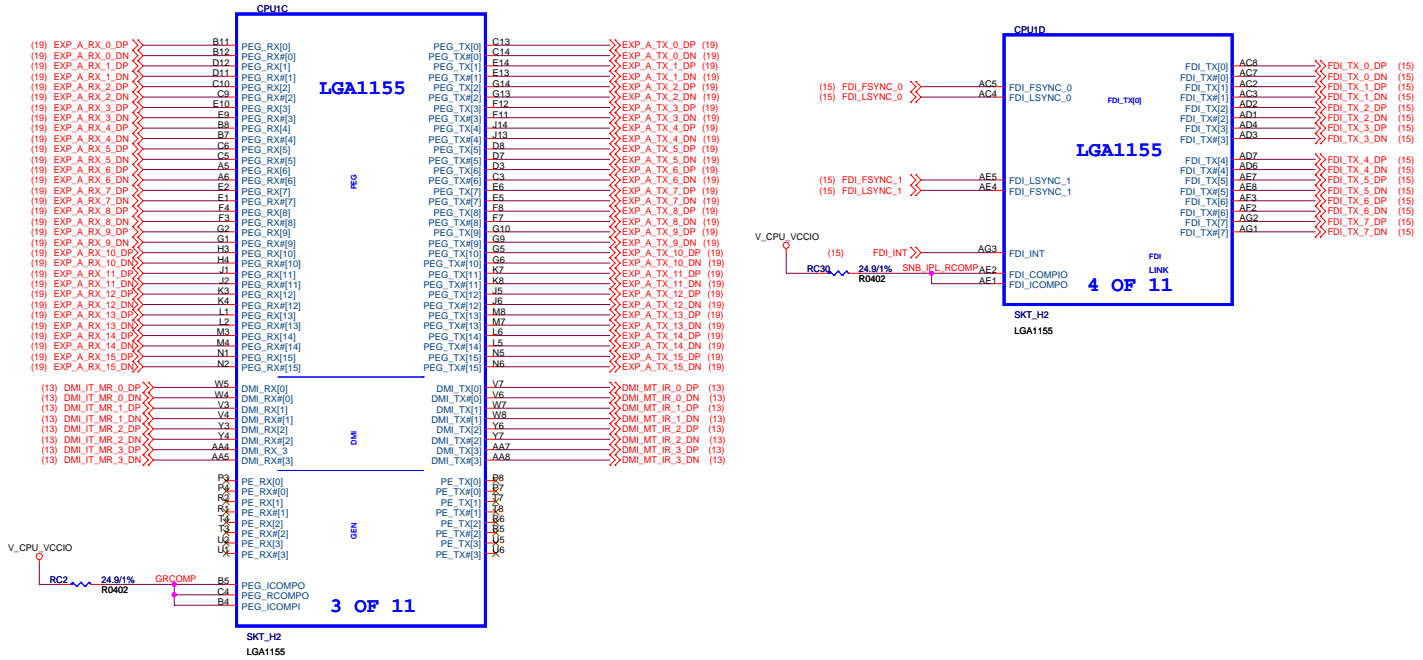
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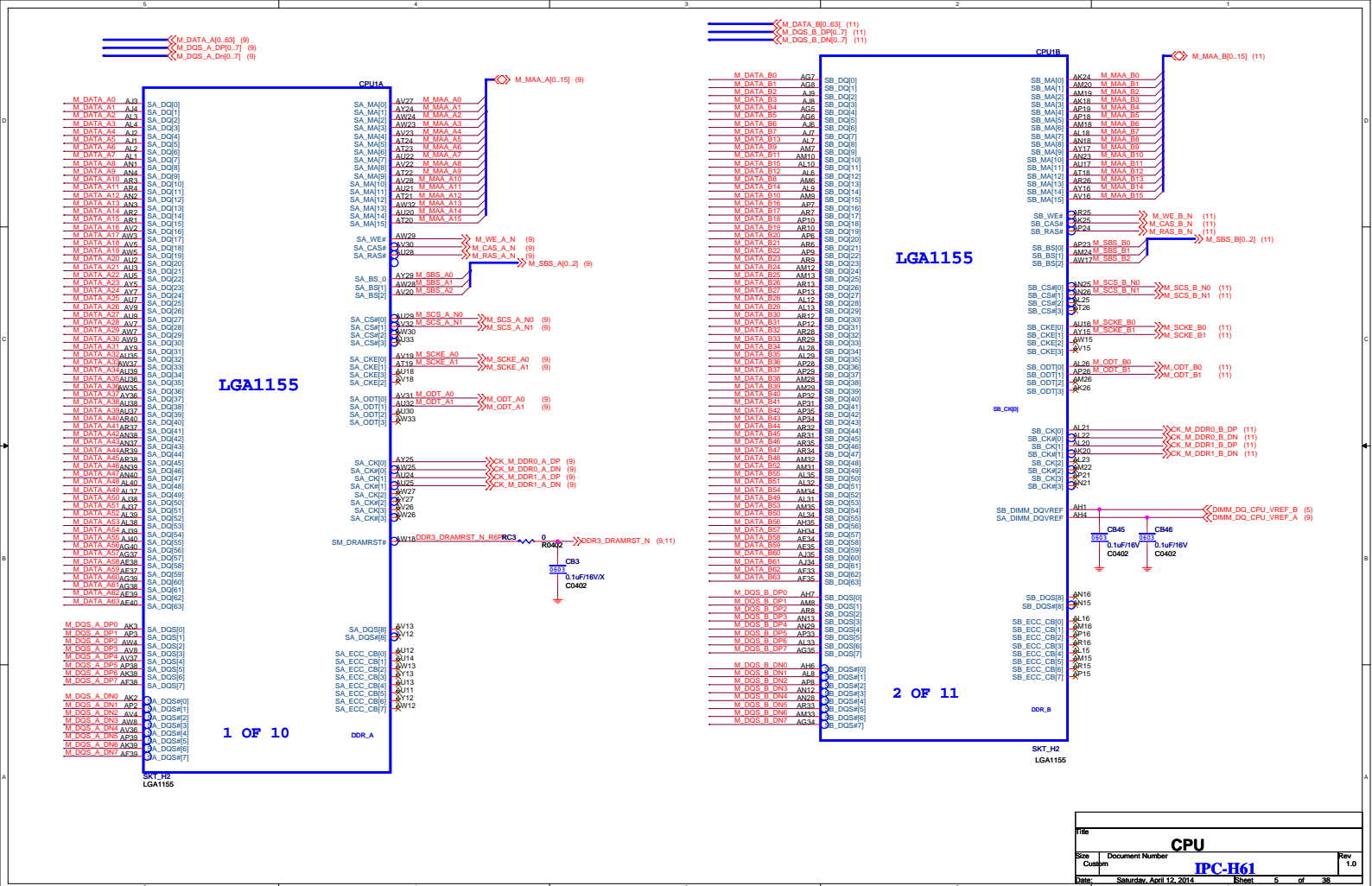
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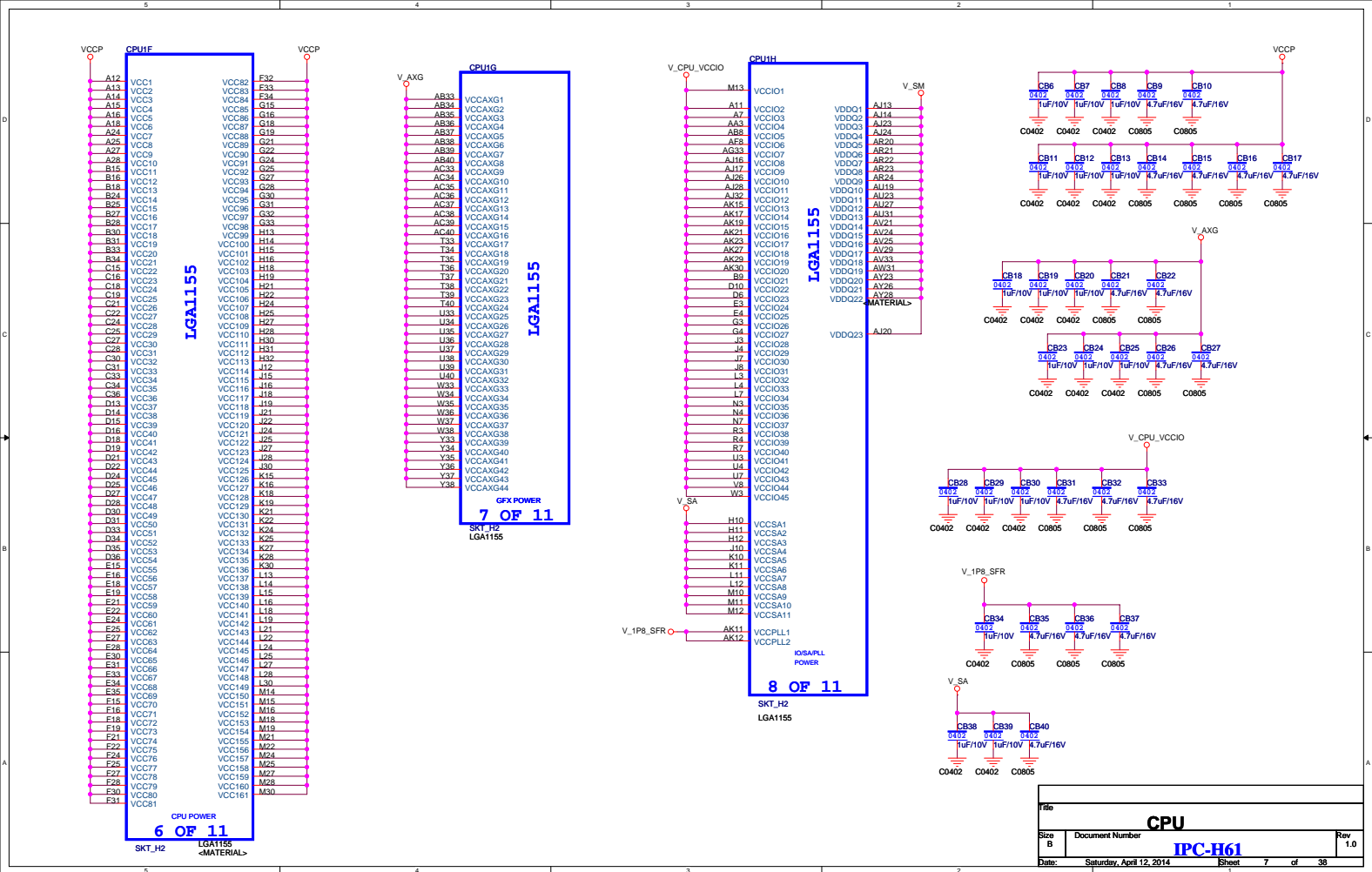
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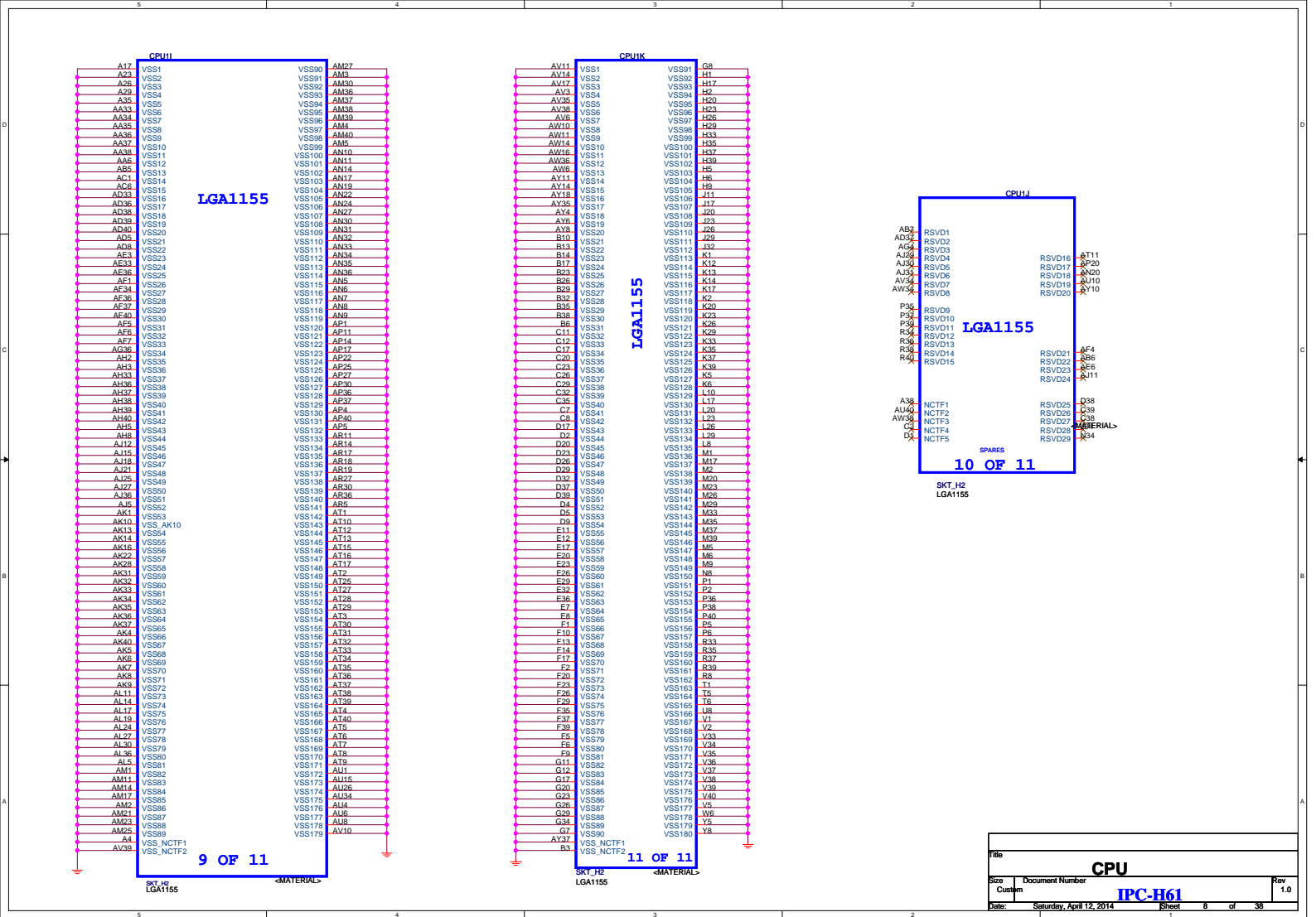
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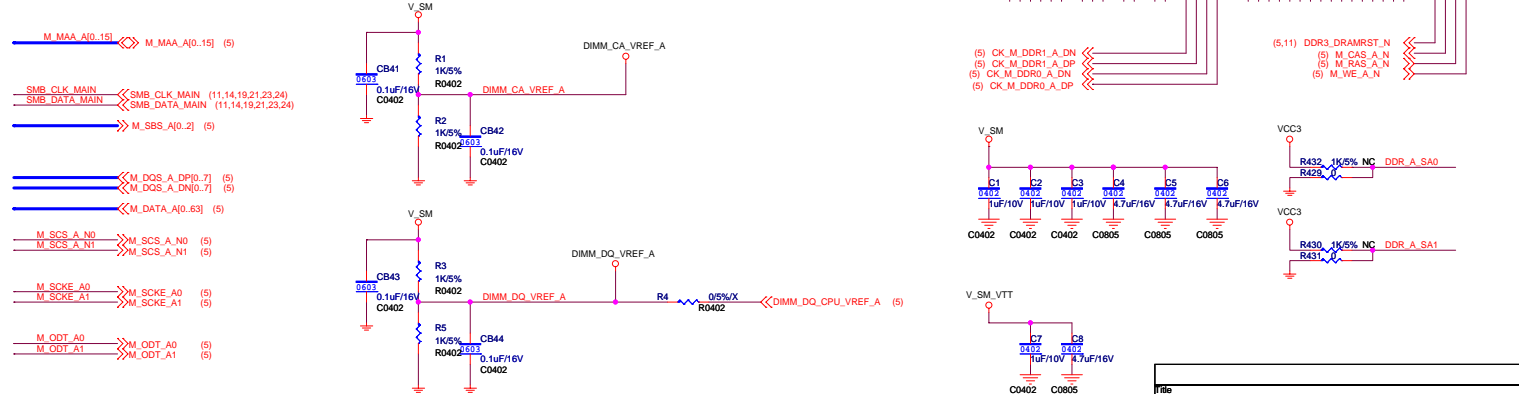
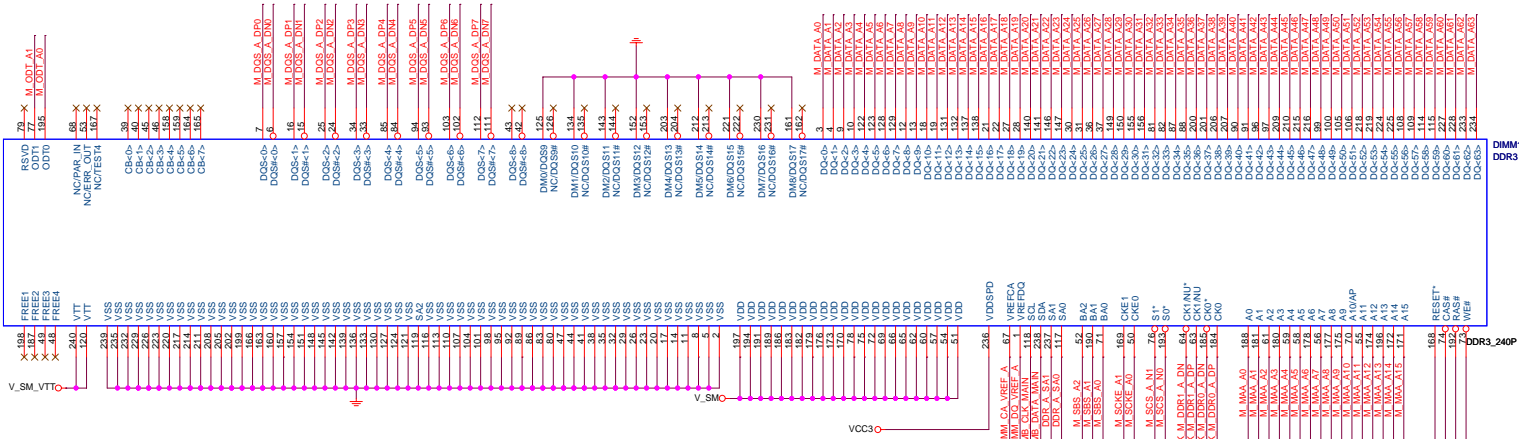


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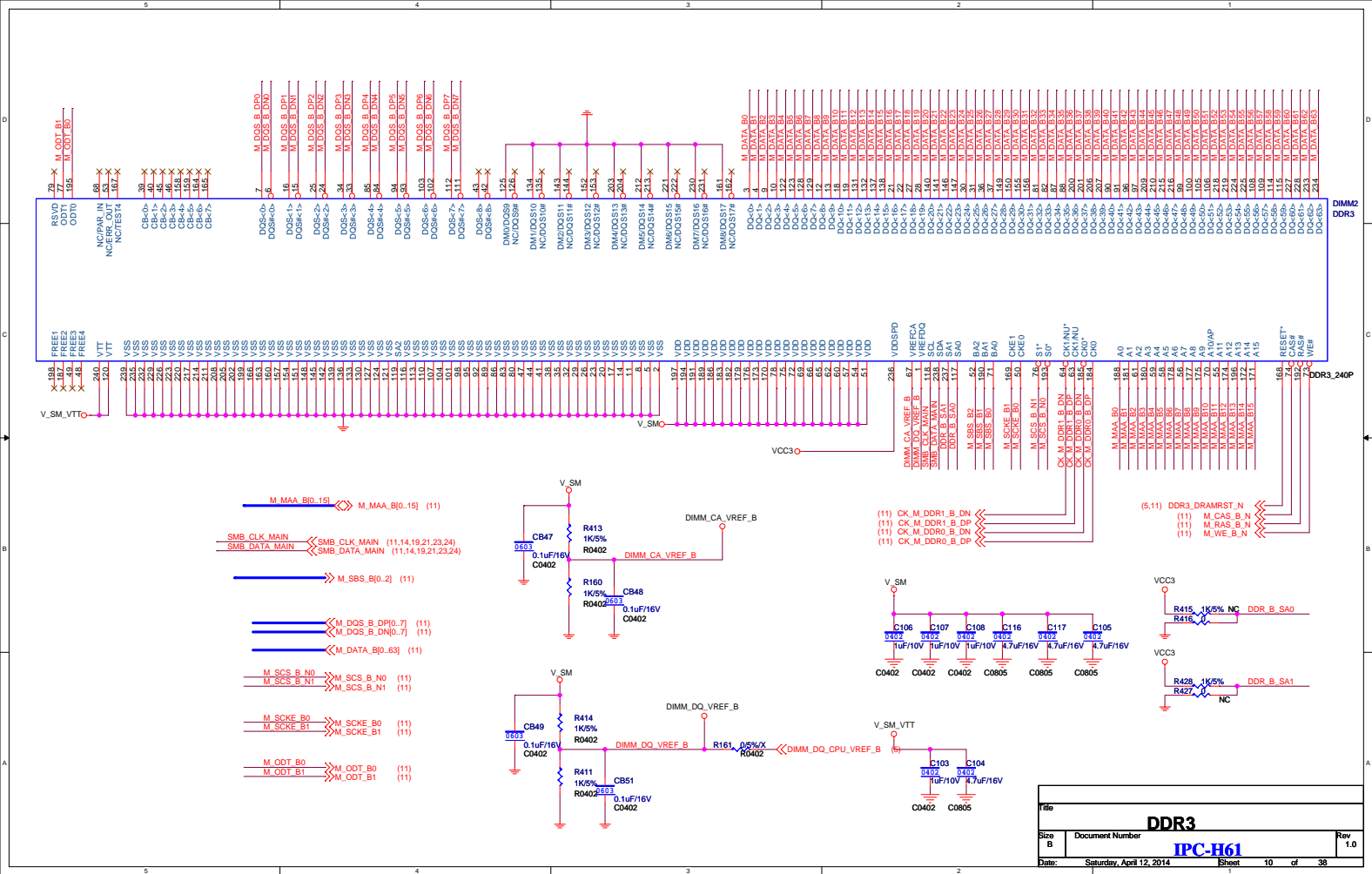


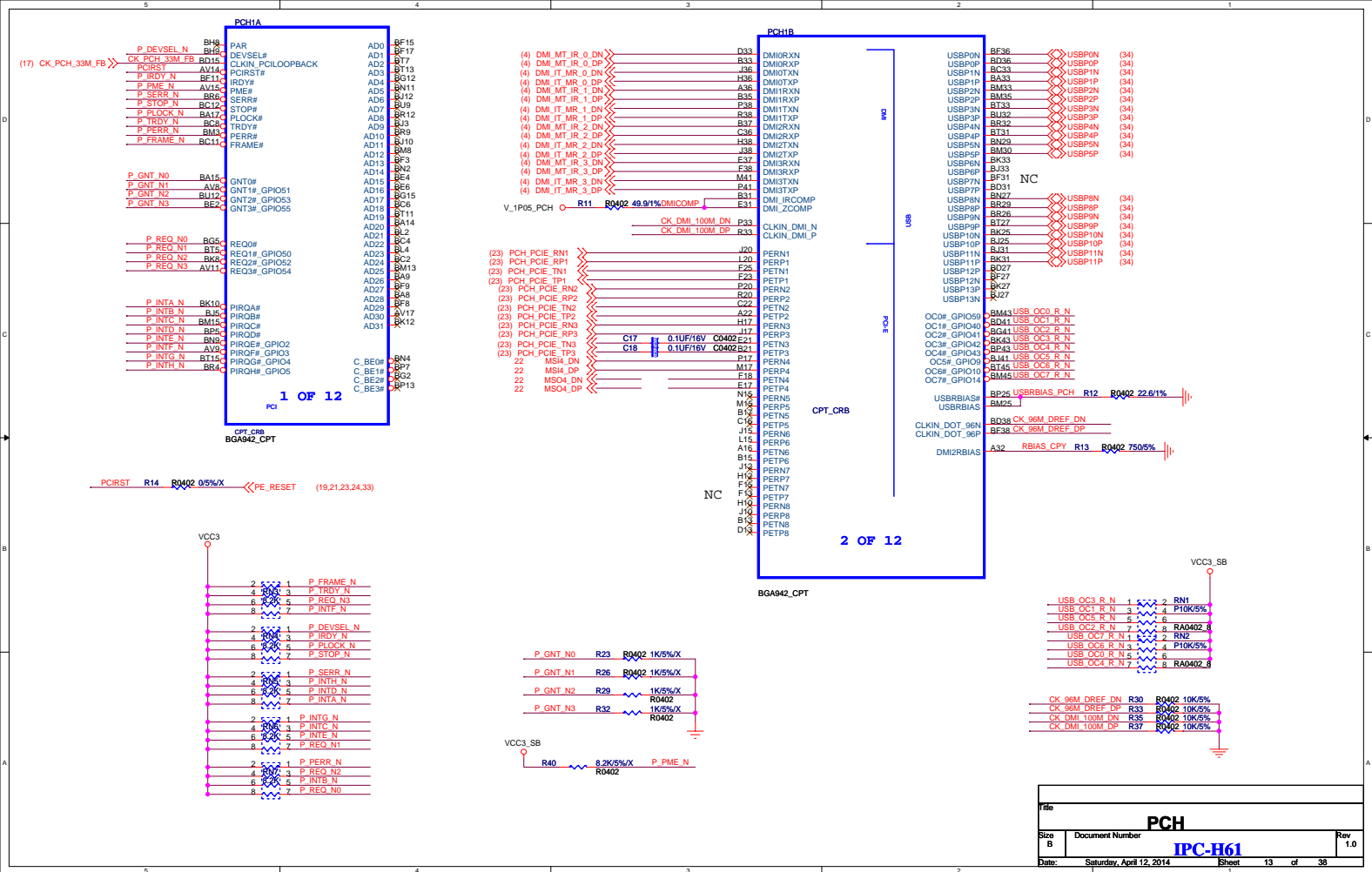




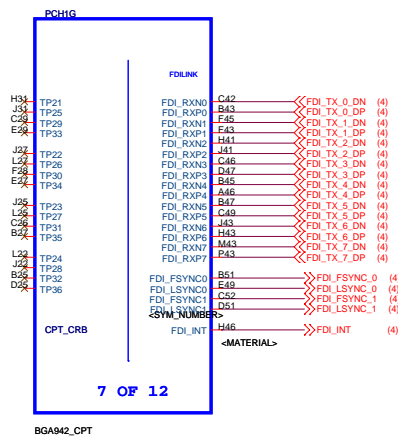
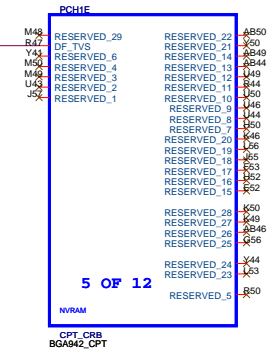


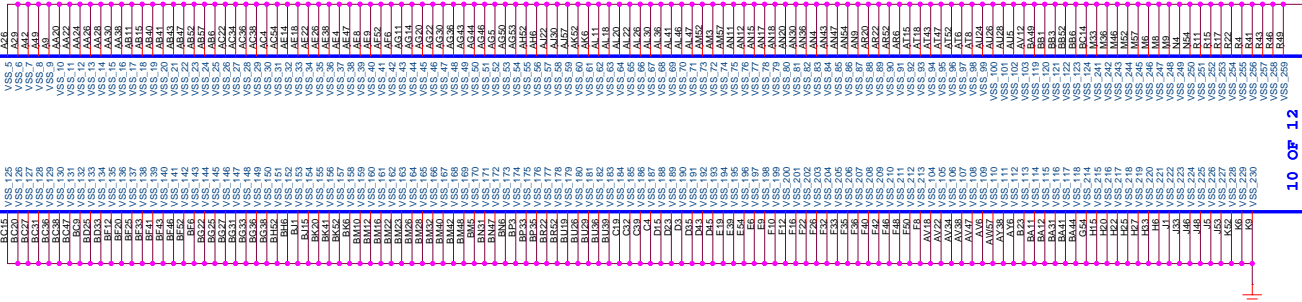
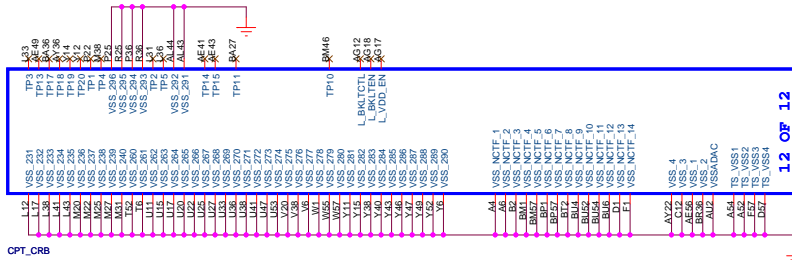
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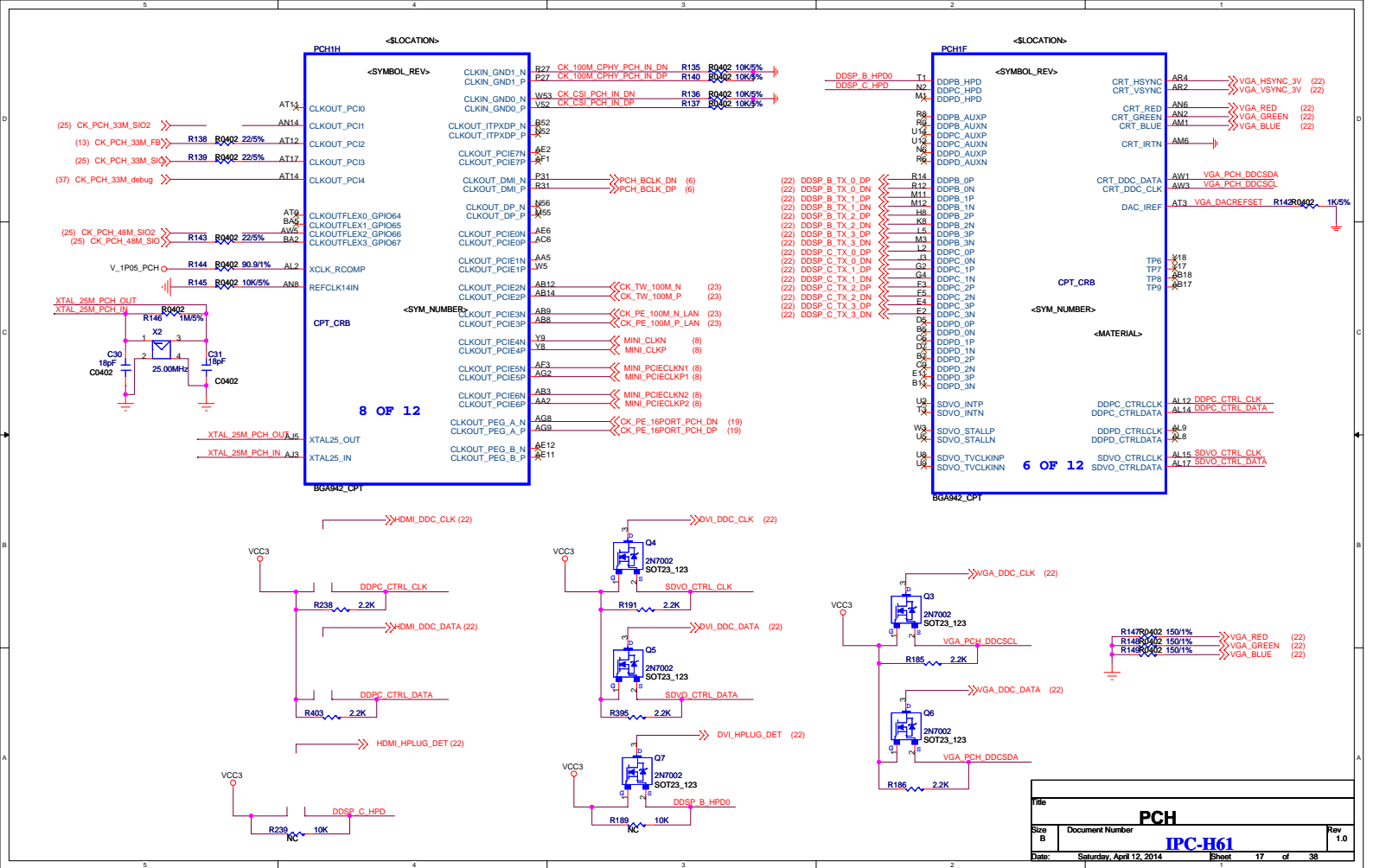


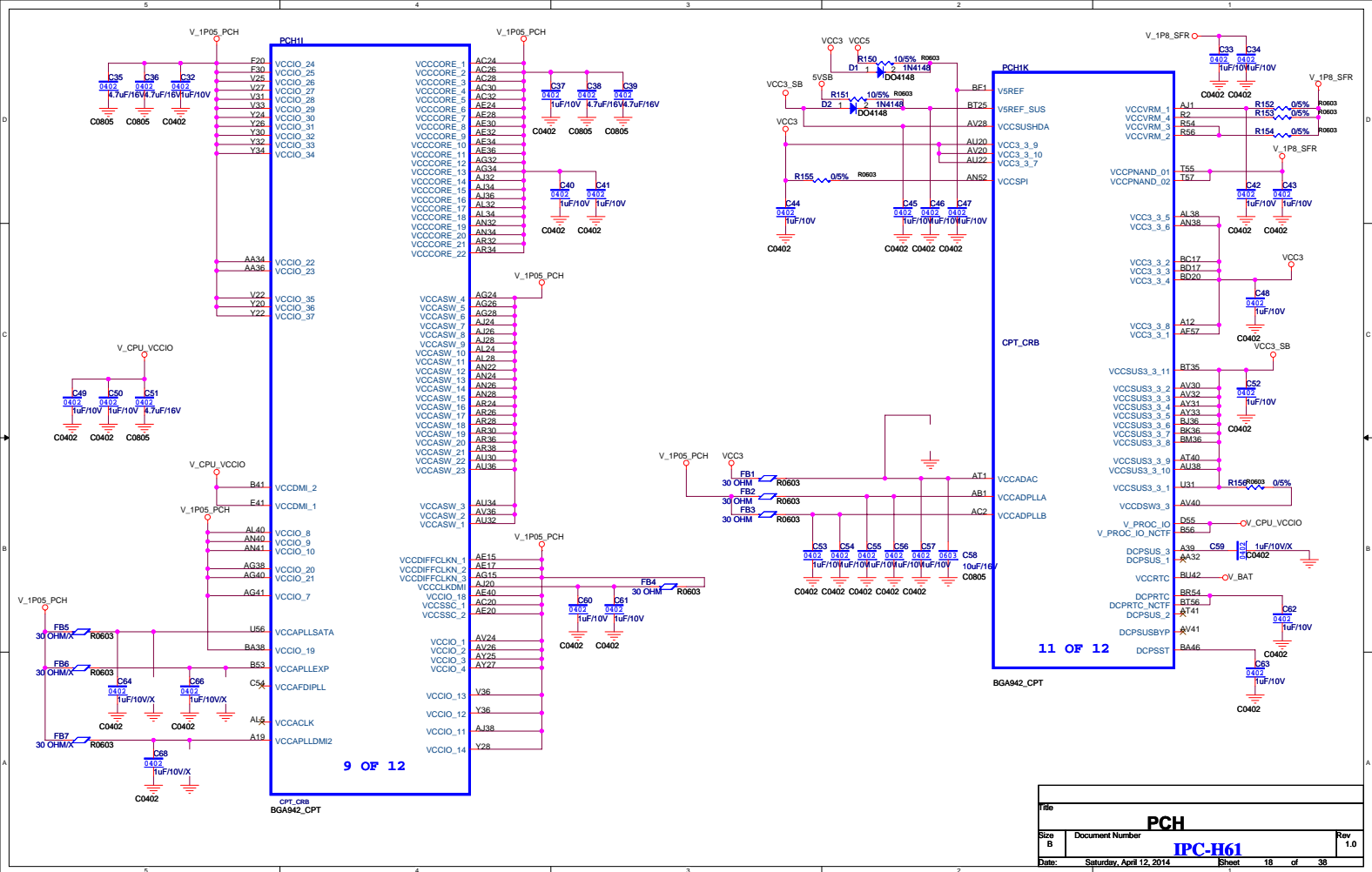


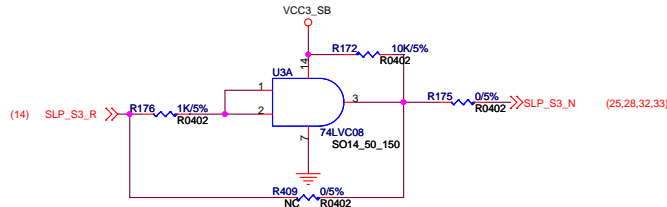
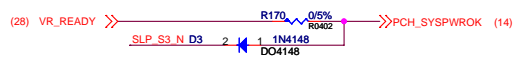
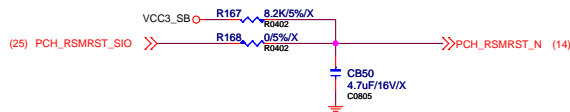
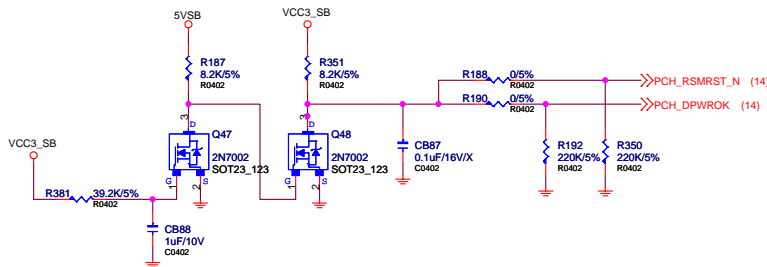
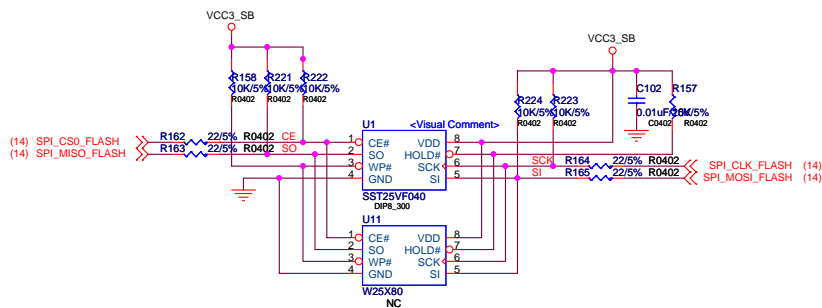


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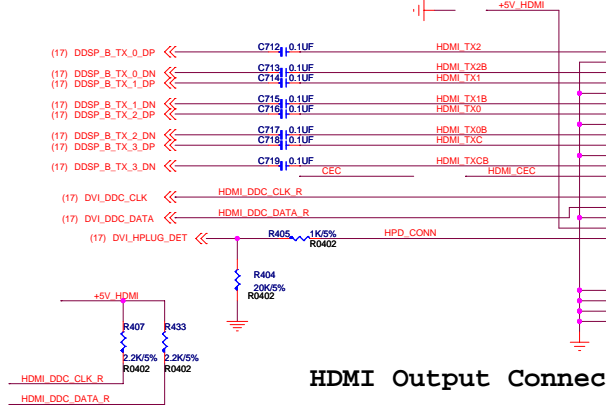
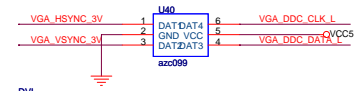
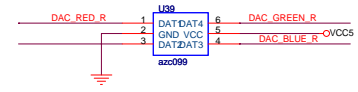
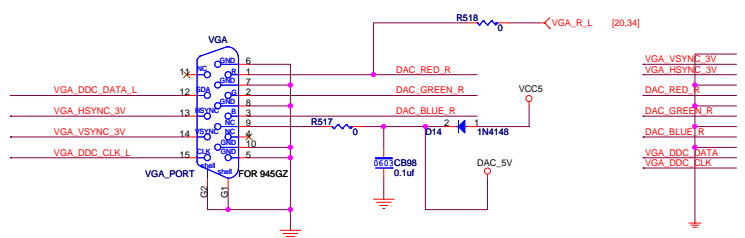
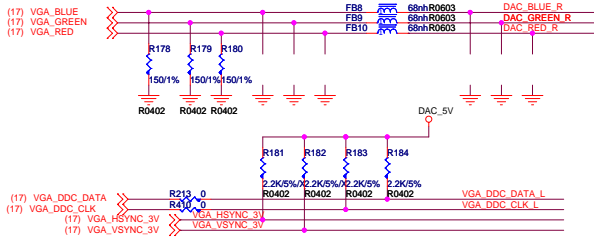
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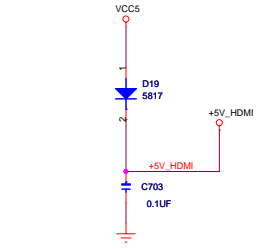
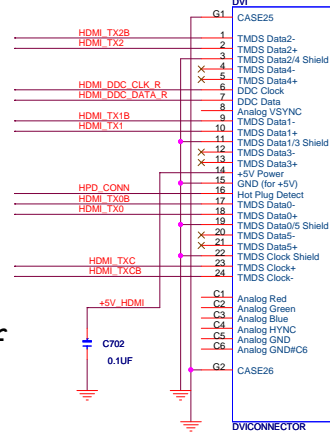




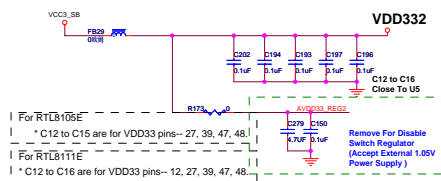
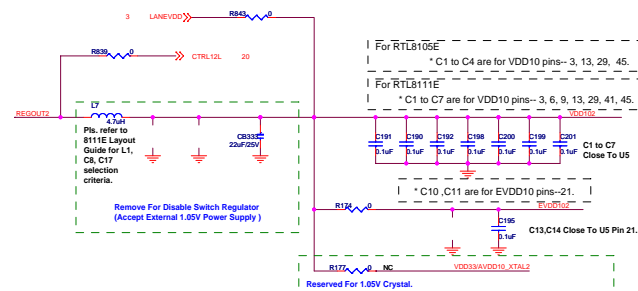
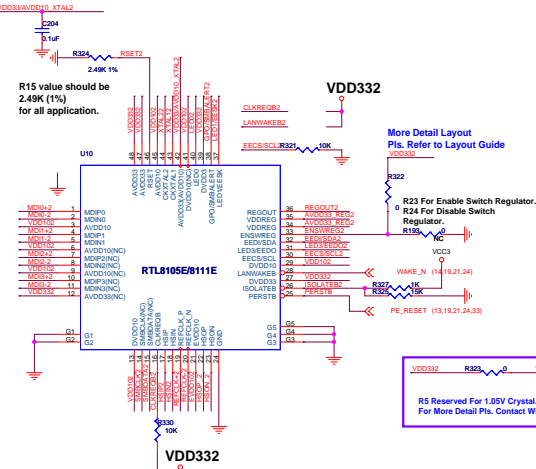
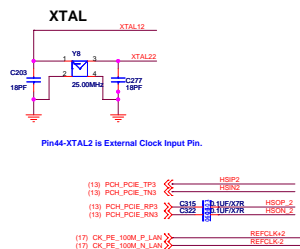
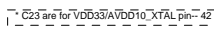
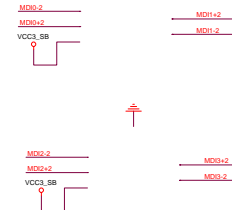
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HDMI Output Connector



VGA/DVI CON			
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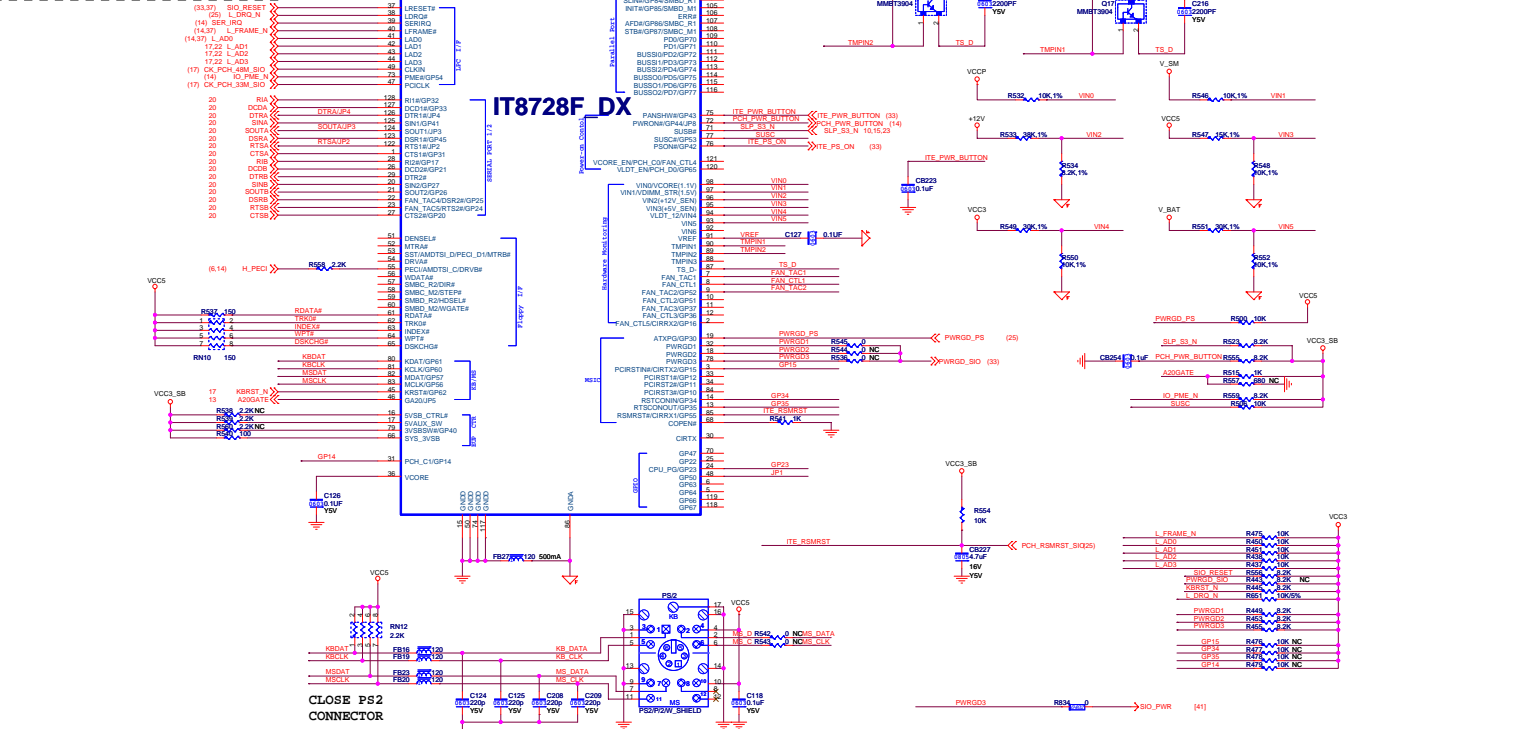


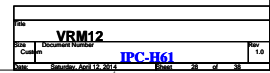
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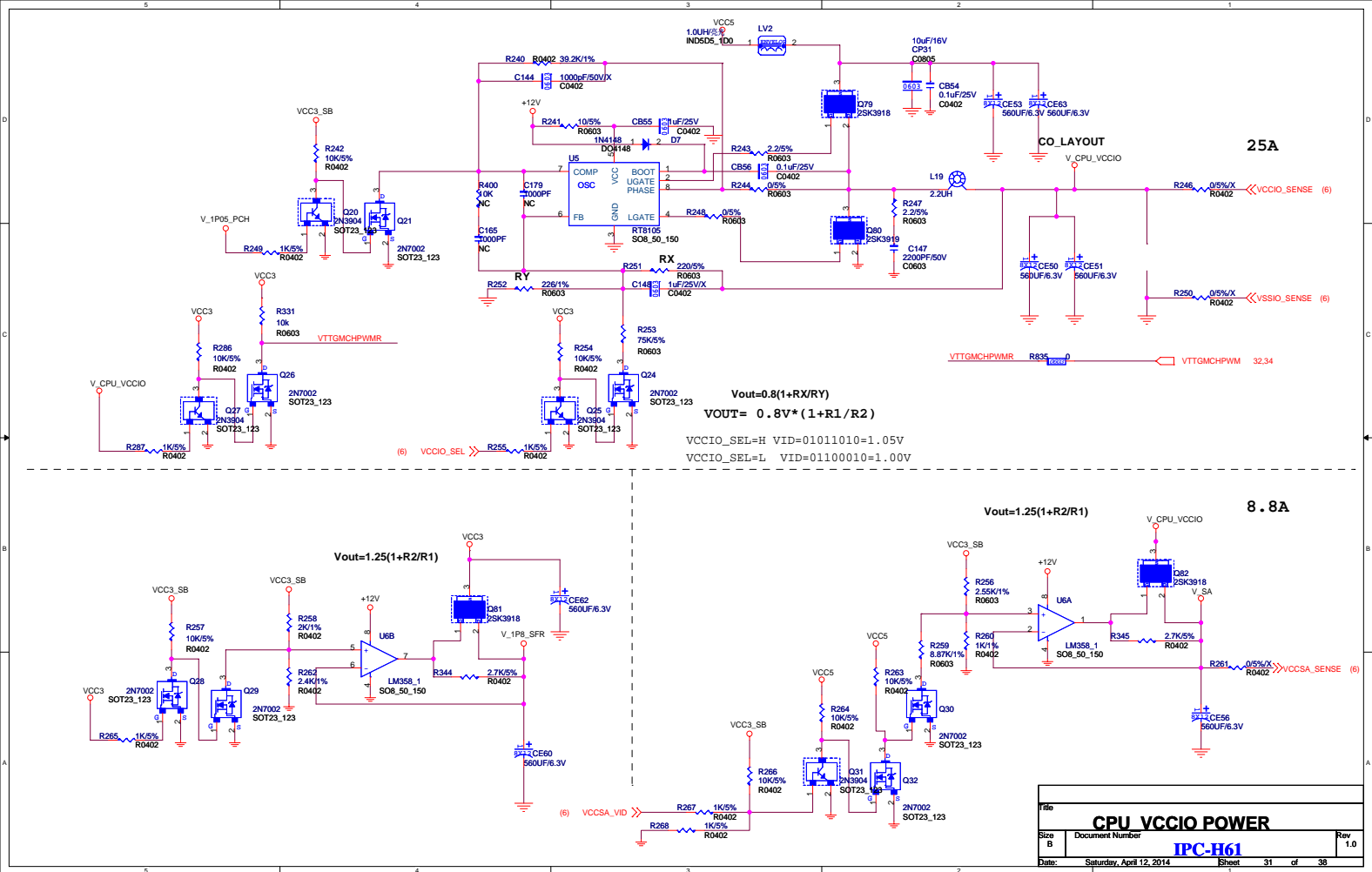
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Power-On Strapping			
Symbol	Value	Description	
JP1	DSW_RUP_SEL	1	RUP
Pin-48		0	DSW
JP2	WDT_EN	1	Disable WDT to reset PWR0K
Pin-122		0	Enable WDT to reset PWR0K
JP3	FAN_CTL_SEL	1	EC Index 63h/6Bh/73h is 80h
Pin-124		0	EC Index 63h/6Bh/73h is 00h
JP4	KBPWR_EN	1	Disable KB Power Sequence
Pin-126		0	Enable KB Power Sequence
JP5	UOVMODE_SEL	1	Notice Mode (Default)
Pin-29	OV/UV	0	Force Mode

NOTE:
NOTE: Use RUP function Pin 71, Pin 72, Pin 73, Pin 77
Pin 88, Pin 77 please pull high to SYS_3VSB.
Pin 16, Pin 75, Pin 76 pull high to 3.3VSB.
If without use these pins, please pull-up to 3.3V.
Don't let it floating
1. Pin 19: SLEEP
2. Pin 71: B0SS#
3. Power On Strapping Options pin
4. Pin 80-83: KCLK/ADAT/MCLK/NGAT



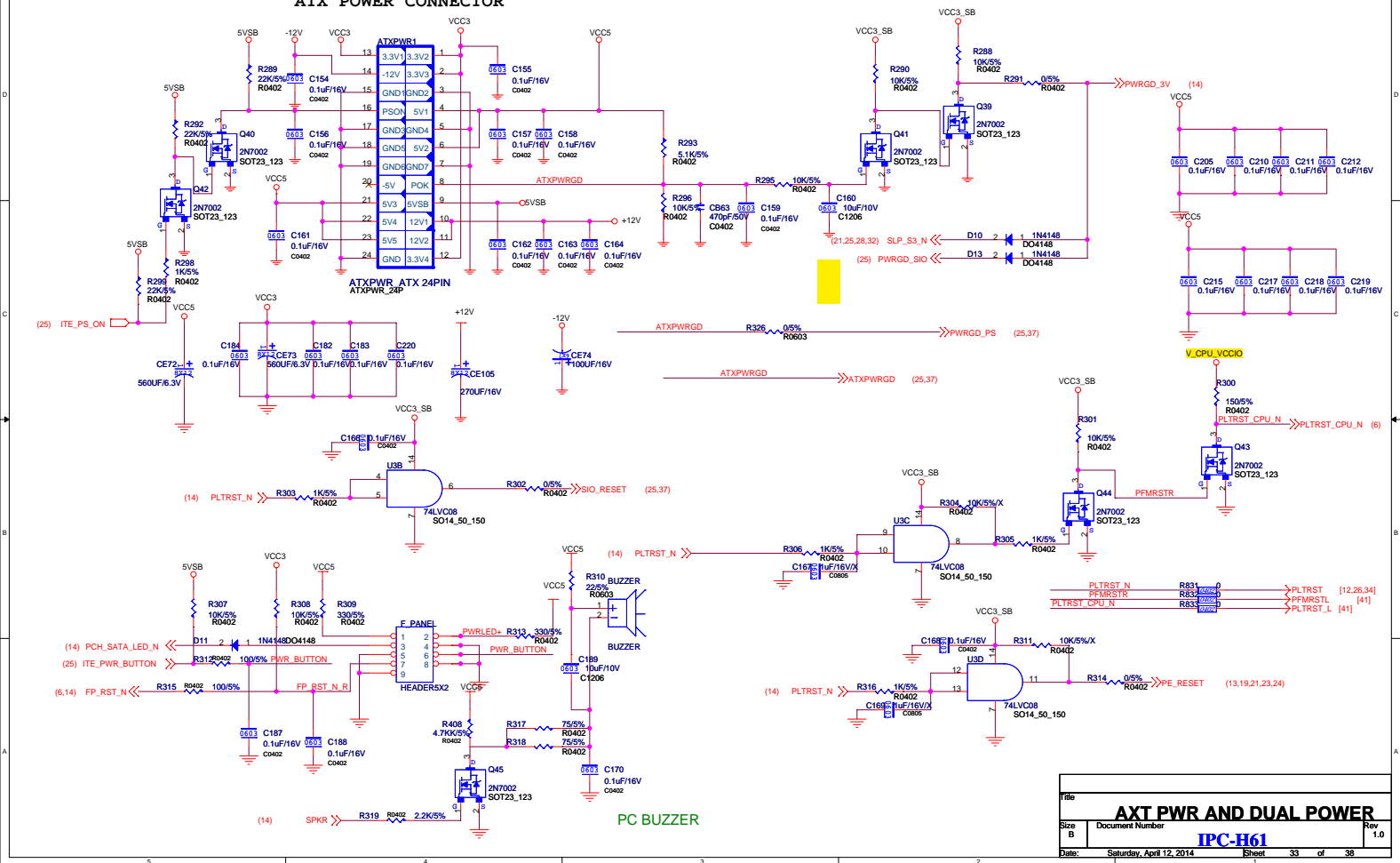




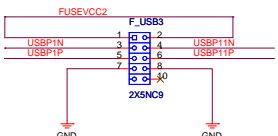
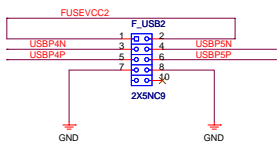
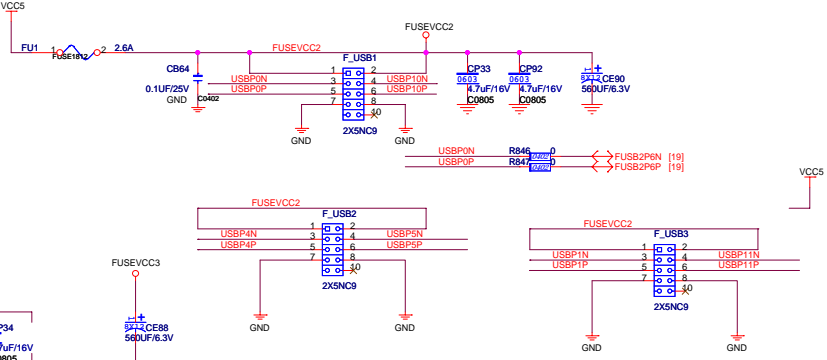
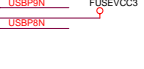
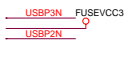
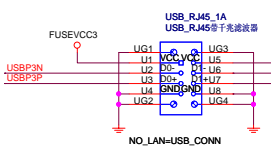
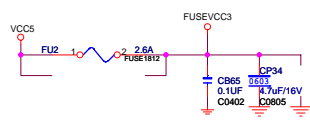
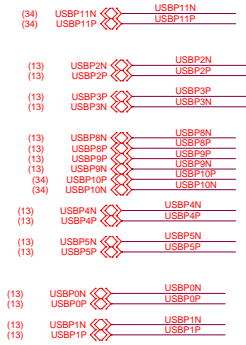


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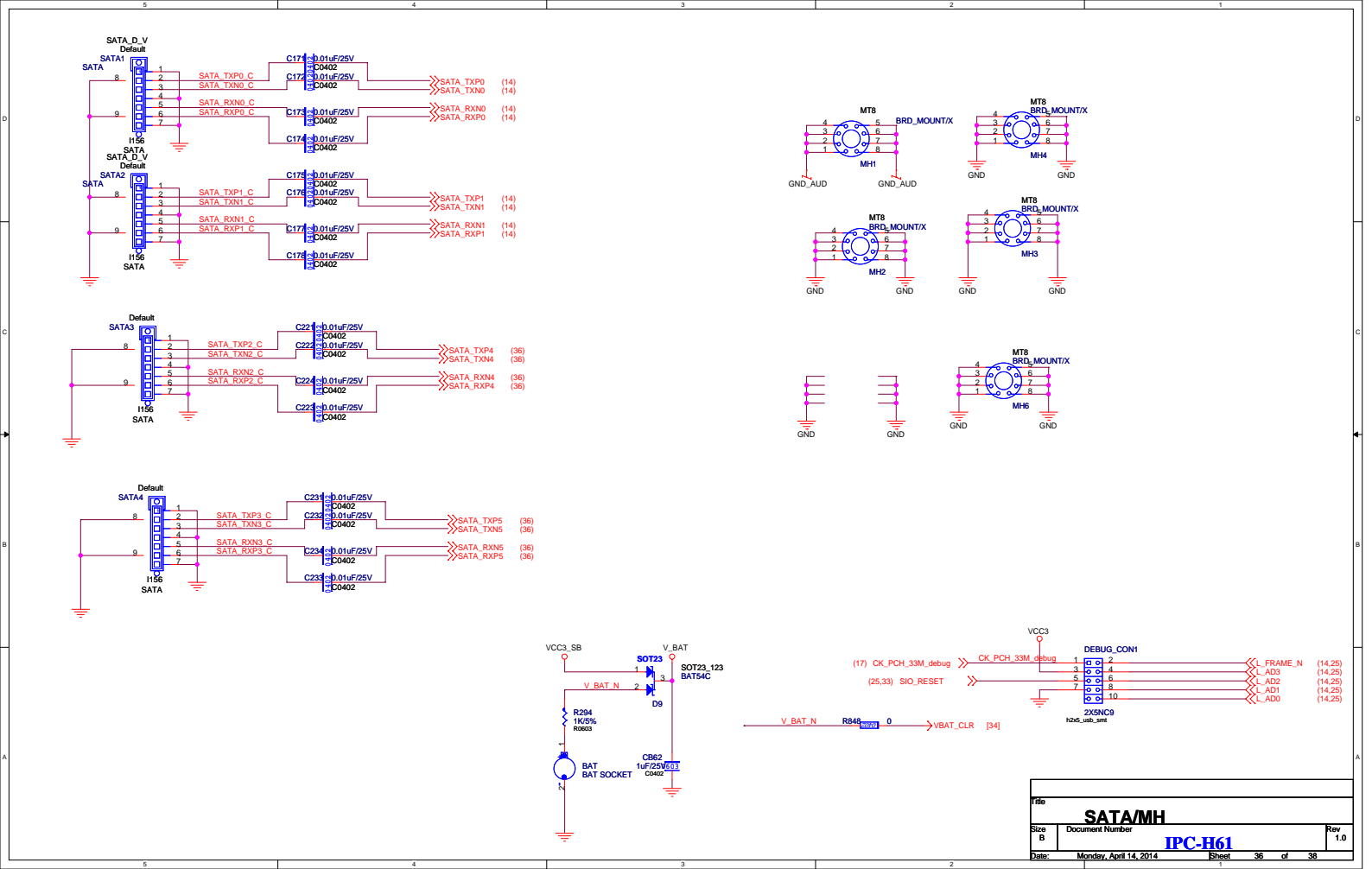
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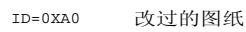


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